



## DECLARATION AND POWER OF ATTORNEY

As the below-named inventors, we declare that:

Our residences, post office addresses, and citizenships are as stated below under our names.

We believe we are the original, first, and joint inventors of the invention entitled "Integrated-Circuit Implementation of a Storage-Shelf Router and a Path Controller Card for Combined use in High-Availability Mass-Storage-Device Shelves that may be Incorporated within Disk Arrays," which is described and claimed in the specification and claims of Patent Application No. 10/602,529, which we filed in the United States Patent and Trademark Office on June 23, 2003 and for which a patent is sought, and this application in part discloses and claims subject matter disclosed in our earlier-filed Application No. 10/341,835, filed January 13, 2003.

We have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment specifically referred to herein (if any).

We acknowledge our duty to disclose information of which we are aware which is material to the patentability and examination of this application in accordance with 37 C.F.R. § 1.56(a), including material information which occurred between the filing date of said earlier-filed application and the present.

We hereby appoint ROBERT W. BERGSTROM, Registration No. 39,906; of the firm Olympic Patent Works PLLC, 401 Second Ave. South, Suite 750, Seattle, Washington 98104, as our attorney to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. Please direct all telephone calls to Robert W. Bergstrom at 206.621.1933 and telecopies to 206.621.5302.

We further declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that the making of willfully false statements and the like is punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and may jeopardize the validity of any patent issuing from this patent application.

2

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3

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